DIGITAL LOGIC AND BOTH 1 WIRE BUS ARE ON SEPARATE SUPPLIES WITH OVERCURRENT ISOLATION

POWER UP LOGIC

GOOD POWER START TIME

LOW POWER START TIME

POWER DOWN LOGIC

NOTE(S):
1. ALL JUMPERS TO BE REPLACED WITH LINKS BEFORE SHIPPING
2. TIMING JUMPERS SELECT 2 MIN TO 4.5-HOURS
3. INITIAL STATE NOT DEFINED USE START SWITCH TO ENSURE ON 4. START SWITCH DOES NOT RESET POWER DOWN LOGIC

Vcc

+24V

BYPASS CAPACITOR (ONE PER LOGIC IC)

Vbb

GND

+24V

FORCE ON

START

INPUT

GOOD POWER START TIME

LOW POWER START TIME

UNUSED GATES

TITLE: REDUCED SUPERVISOR CONTROL BOARD

FILE: Supervisor_Control_Border.sch

REVISION: V001

DRAWN BY: DLV - DESIGN JS